

Electronic Acknowledgement Receipt

EFS ID:	7505576
Application Number:	10595157
International Application Number:	
Confirmation Number:	3496
Title of Invention:	Method of Manufacturing Multi-Layer Circuit Board
First Named Inventor/Applicant Name:	Toshiaki Takenaka
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Attorney Docket Number:	2006-0223A
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Application Type:	U.S. National Stage under 35 USC 371

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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	AttachA_RCE_P04030802.pdf	57209	no	1
			d7249c43ed713540a842495679a5af0fccd3e24d		

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2	Fee Worksheet (PTO-875)	fee-info.pdf	31909	no	2
			4e61565be3e6c0f94cc22237f13e3c0b0f8a72e2		

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